

IN THE CLAIMS

1-11. (Canceled)

12. (Currently amended) A configurable image processing device to read and output an image data comprising an input unit to get an input image and an output unit that provides a particular output format depending on a final device that receive such output image data, a selection unit that selects a particular group of atomic operations to be performed on the basis of a performance of said final device performance, so creating a specific sequence of said atomic operations providing meaningful information for said the selected final device, at least first and second units of processing called "transforms" configured in a dynamic pipeline to perform said specific sequence of atomic operations, wherein said first and second transforms process data in a context independent fashion, that being there is not implied knowledge coupled between said first and second transforms influencing the means of process data.

13. (Currently amended) A configurable image processing device according to claim 12, further comprising ~~at least the following major subsystems:~~

1. ~~unit of processing called "transforms";~~
2. ~~a unit of work called "packets" and a;~~
3. ~~unit of transportation called "ports",~~
~~said first and second subsystems units of processing, work and transportation~~
providing for transformations of input image data, independent data representation and connections between ~~the said first and second~~ transforming units.

14. (Canceled)

15. (Currently amended) A configurable image processing device according to claim 12, further comprising a sequence of basic operations providing for a remapping architecture.

16 and 17. (Canceled)